**UDP1 Verilog code**

**A computer screen shot of a code

Description automatically generated**

**UDP2 Verilog code**

**A computer screen shot of a computer code

Description automatically generated**

**Full Adder Module**

**A screenshot of a computer program

Description automatically generated**

**Test-Bench for Full Adder**

**A screenshot of a computer program

Description automatically generated**

**Netlist of Full Adder**

**A diagram of a computer

Description automatically generated**

**Four-bit Input Adder**

**Verilog Code for one bit full adder**

**A math equations on a white background

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**Verilog Code for four bit full adder**

**A screenshot of a computer program

Description automatically generated**

**Netlist of four bit Full Adder**

**A diagram of a computer program

Description automatically generated**

**Wave Form four bit full adder**

**A screenshot of a computer

Description automatically generated**

**Part 2**

**Verilog Code for divBy3**

**A screenshot of a computer program

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**Test-Bench for diivBy3**

**A screenshot of a computer program

Description automatically generated**

**Netlist divBy5**

**A diagram of a computer

Description automatically generated**

**Wave Form divBy5**

**A screenshot of a computer program

Description automatically generated**

**Verilog Code for divBy5**

**A screenshot of a computer program

Description automatically generated**

**Test-Bench for diivBy5**

**A screenshot of a computer

Description automatically generated**

**Netlist divBy5**

**A diagram of a computer

Description automatically generated**

**Wave Form divBy5**

**A screenshot of a computer

Description automatically generated**

**Verilog Code for combine divider**

**A computer screen shot of a computer

Description automatically generated**

**Test-Bench for combine divider**

**A screenshot of a computer program

Description automatically generated**

**Netlist divBy5**

**A diagram of a circuit

Description automatically generated**

**Wave Form divBy5**

**A screenshot of a computer

Description automatically generated**